

FIG. 1

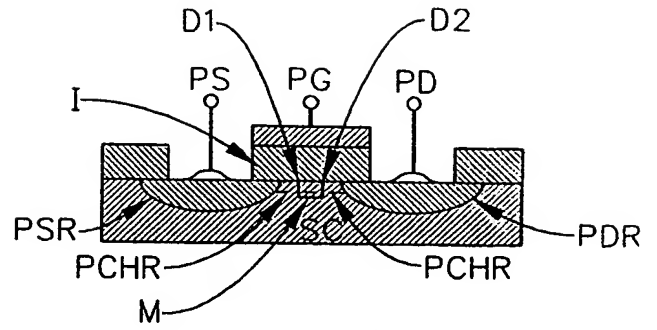


FIG. 2

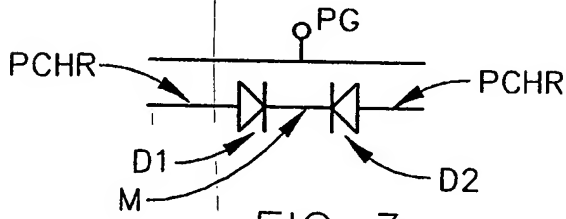


FIG. 3

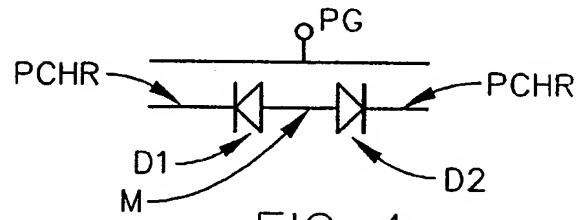


FIG. 4

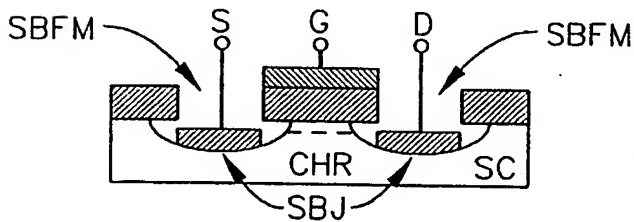


FIG. 5

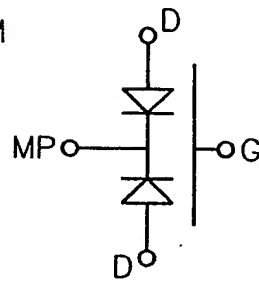


FIG. 6a

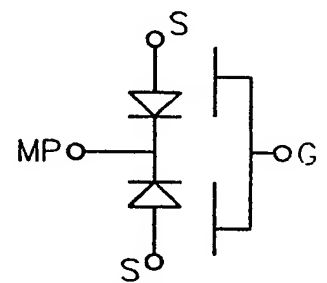


FIG. 7a

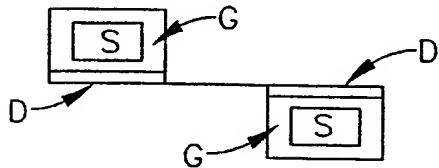


FIG. 9a

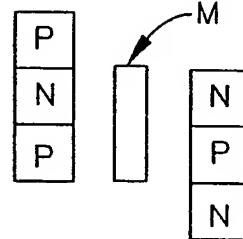


FIG. 9b

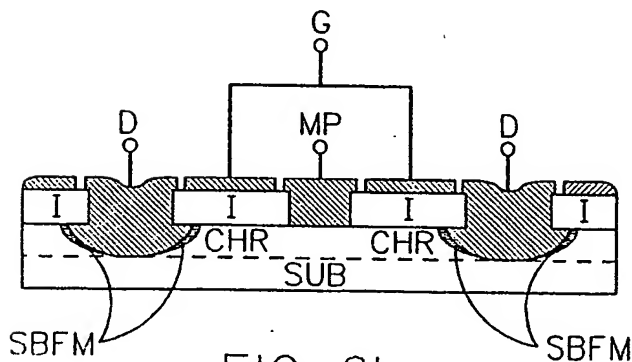


FIG. 6b

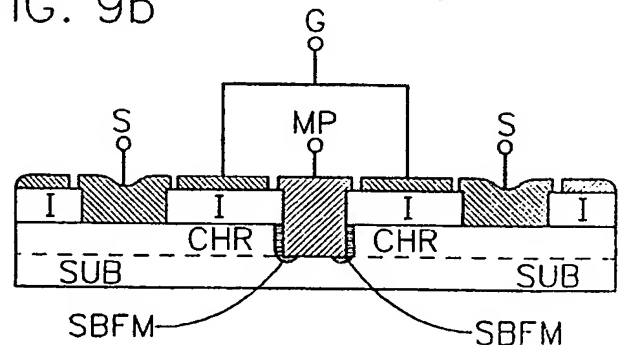


FIG. 7b

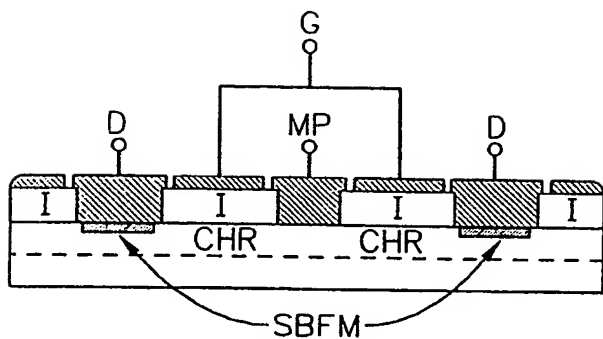


FIG. 6c

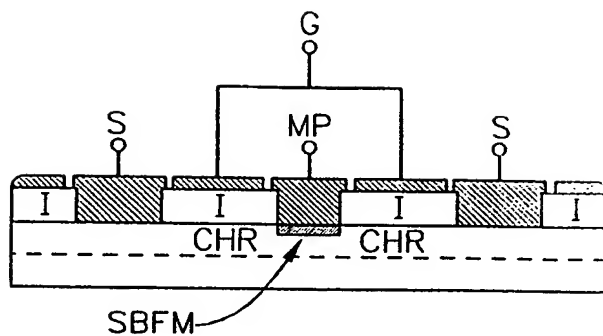


FIG. 7c

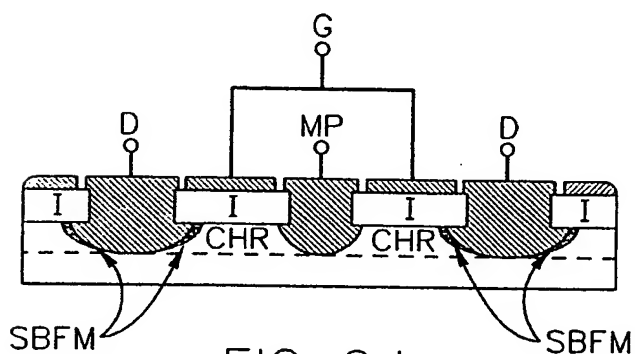


FIG. 6d

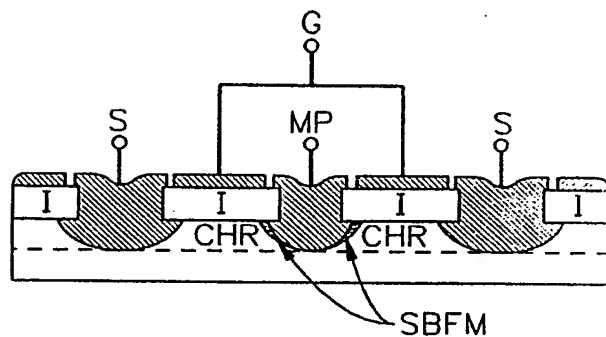


FIG. 7d

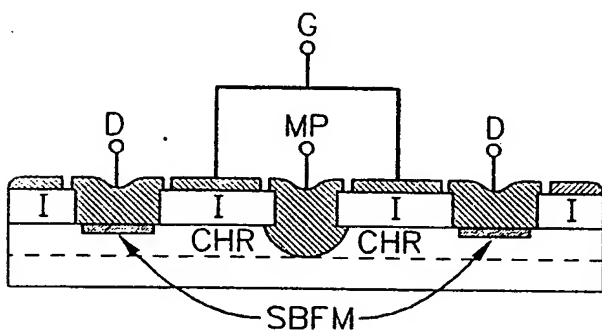


FIG. 6e

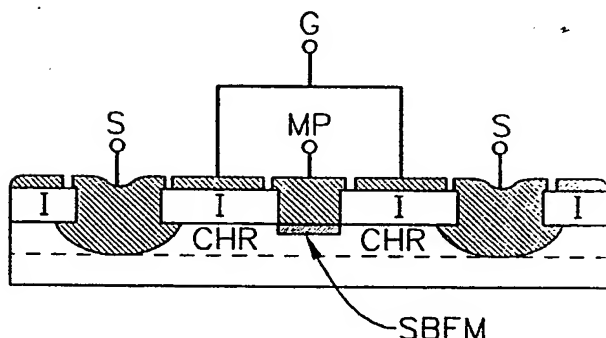


FIG. 7e

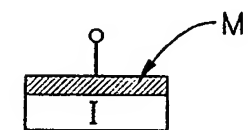


FIG. 6f

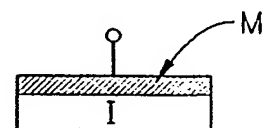


FIG. 7f

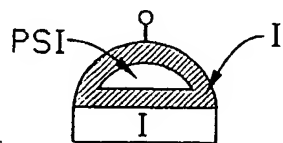


FIG. 6g

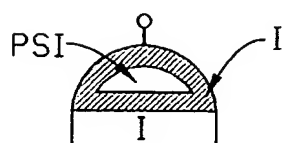
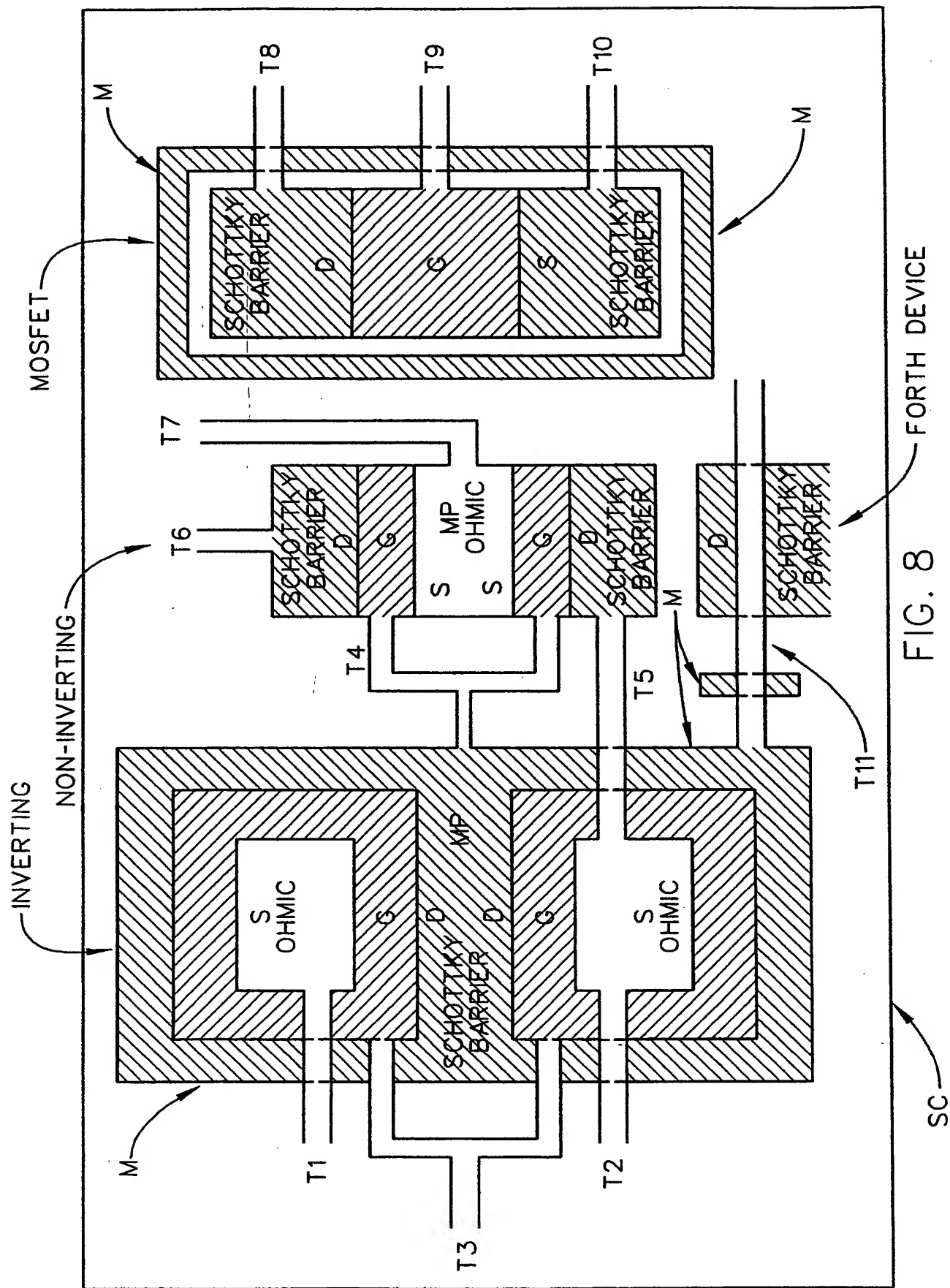


FIG. 7g



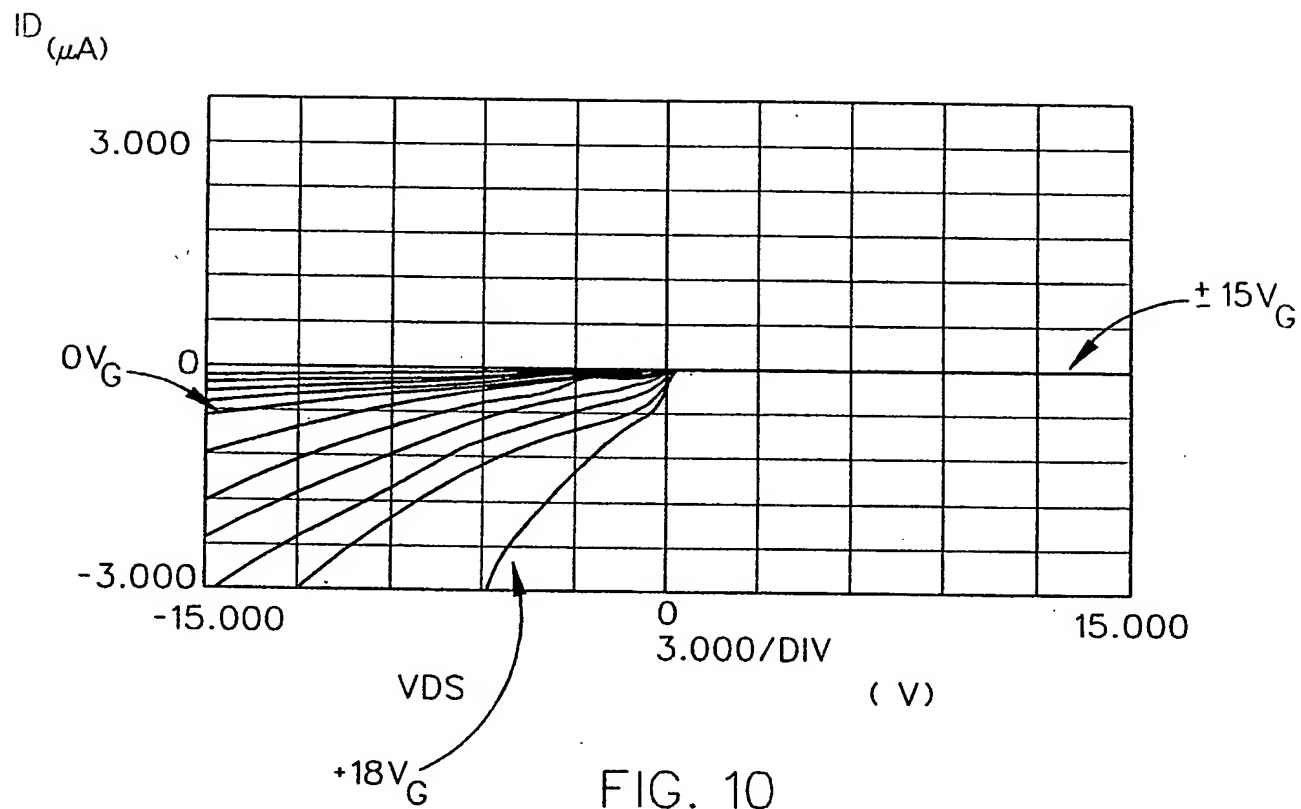


FIG. 10

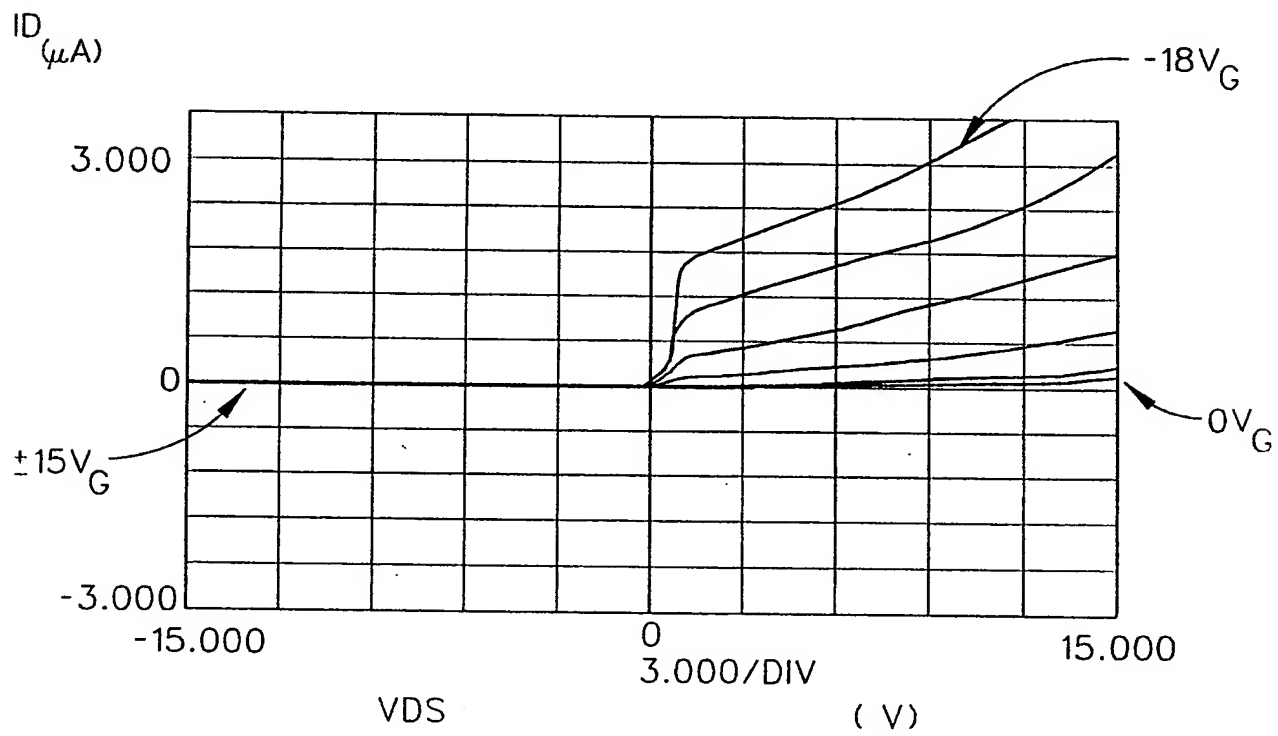


FIG. 11

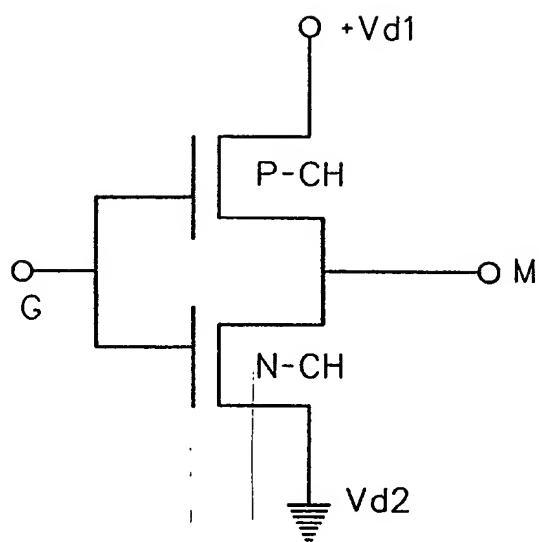


FIG. 12a

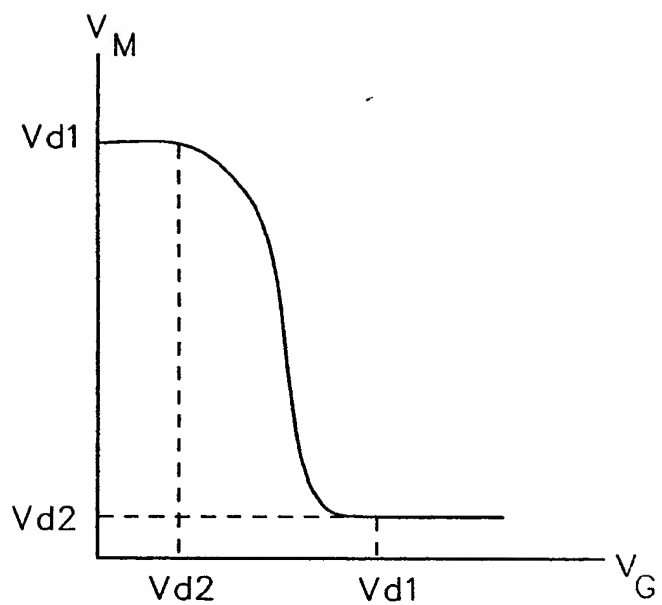


FIG. 12b

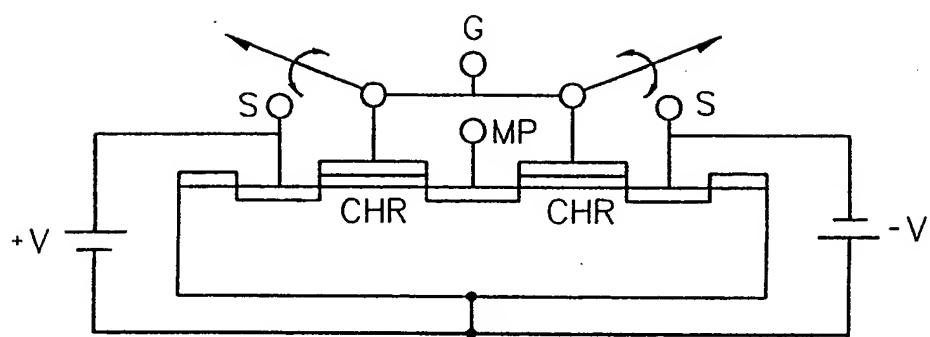


FIG. 13a

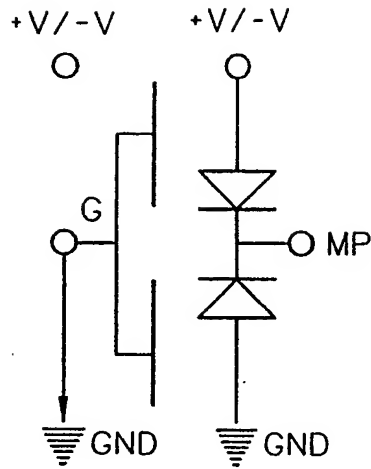


FIG. 13b

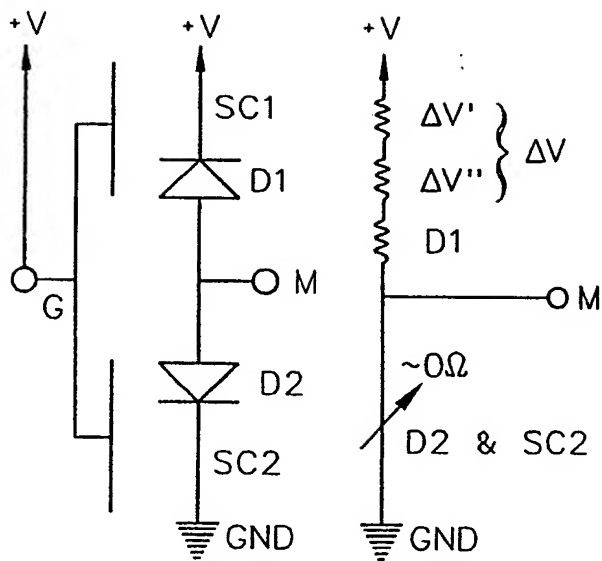


FIG. 14a

$\Delta V'$ = PINCHOFF VOLTAGE DROP

$\Delta V''$ = SILICON CHANNEL
VOLTAGE DROP

$\Delta V = \Delta V' + \Delta V''$ & IS "OFF"
HALF SILICON CHANNEL
CARRIER INDUCING
GATE VOLTAGE

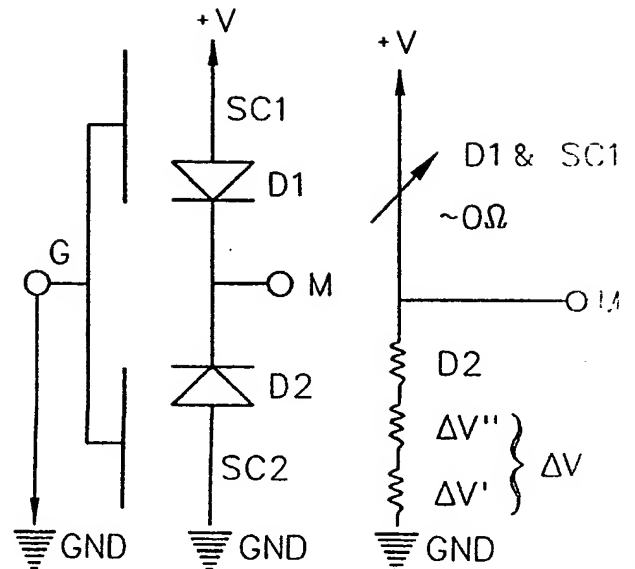


FIG. 14b

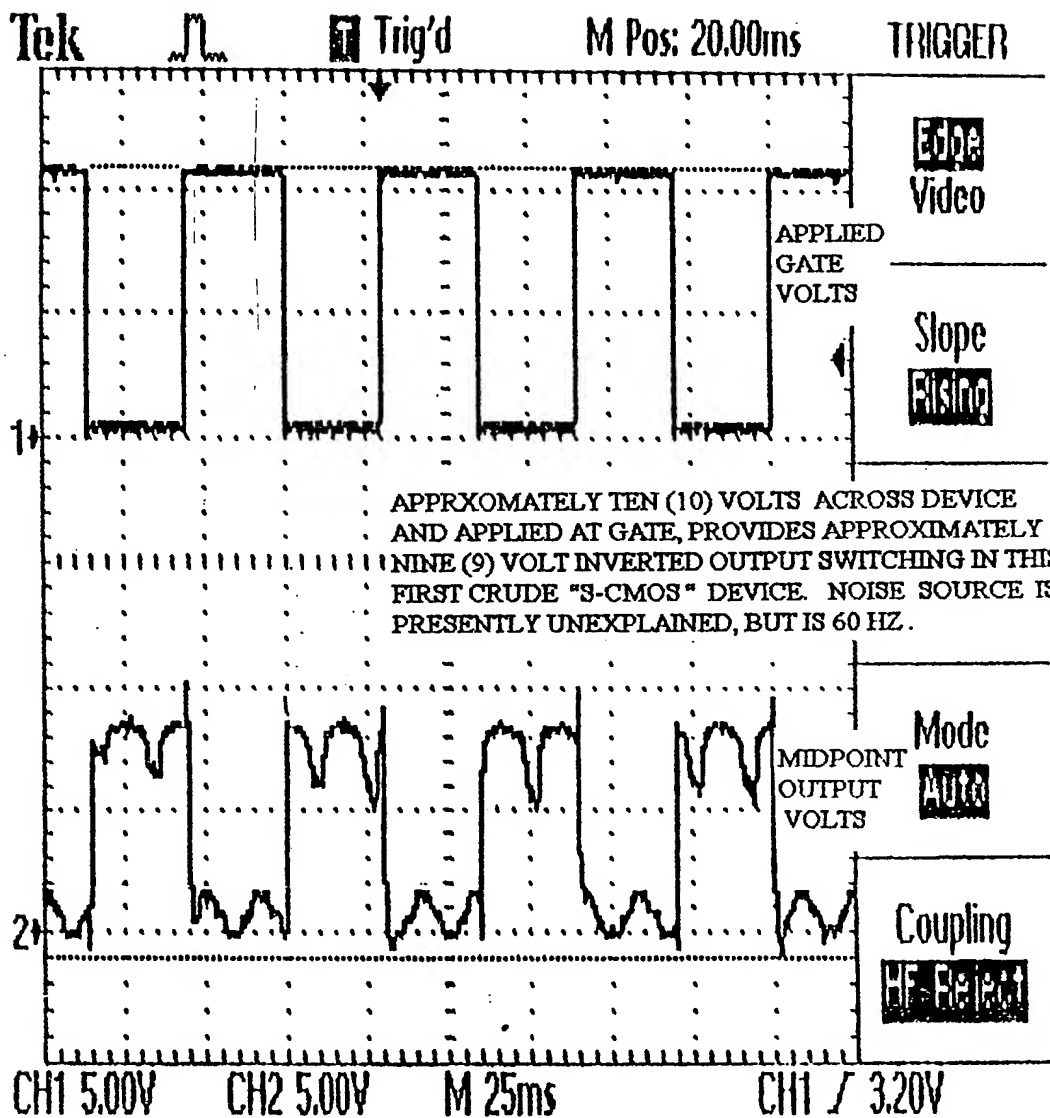


FIG. 15